

FEATURES

Fast response, instantly clamping the transient over voltage.

- High surge current handling capability.
- High energy absorption capability.
- Low clamping voltages, providing better surge protection.
- Low capacitance values, providing digital switching circuitry protection.
- High insulation resistance, preventing electric arcing to the adjacent

devices or circuits.



APPROVALS

RoHSCompliance with 2011/65/EUHFCompliance with IEC61249-2-21:2003

APPLICATIONS

Universal Serial Bus (USB).	
Mobile communication.	
Computer/DSP product.	
Video and audio ports.	
Portable/Hand-Held Products.	

ELECTRICAL SPECIFICATION

Data, Diagnostic I/O ports.

Technical Data Symbol Value Unit Maximum allowable continuous AC voltage at 50-60Hz 9 V V_{RMS} Maximum allowable continuous DC voltage V_{DC} 12 V 125 V Varistor breakdown voltage V, Typical capacitance value measured at 1MHz С 2.5 рF Typical capacitance value tolerance t ±30 % Maximum allowable clamping voltage V_{c} 200 V Leakage current at VDC (at initial state) <1 μΑ LDC μΑ Leakage current at VDC (after ESD test) I_{LDCA} <2 Response time <1 T_{rise} ns ESD Per IEC 61000-4-2 (Air) ± 15 k٧ V_{ESD} ESD Per IEC 61000-4-2 (Contact) ± 8 kV V_{FSD} Operation ambient temperature -55~+85 °C T_{opt} $\mathsf{T}_{_{\mathsf{STG}}}$ °C Storage temperature range -55~+125



RELIABILITY TESTING PROCEDURES

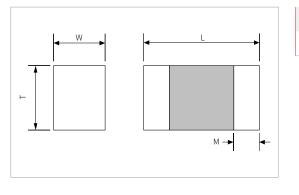
Characteristic	Test method and description					
High Temperature Storage	The specimen shall be subjected to 125°C for 1000 hours in a thermostatic bath without load and then stored at room temperature and humidity for 1 to 2 hours. The change of varistor voltage shall be within 10%.					
Temperature Cycle		Step	Temperature	Period		
	The temperature cycle of specified temperature shall be repeated five times and then stored at room temperature and humidity for one two hours. The change of varistor voltage shall be within 10% and mechanical damage shall be examined.	1	-40±3°C	30min±3		
		2	Room Temperature	1~2hours		
		3	125±2°C	30min±3		
		4	Room Temperature	1~2hours		
High Temperature Load	After being continuously applied the maximum allowable voltage at 125°C for 1000hours, the specimen shall be stored at room temperature and humidity for one or hours, the change of varistor voltage shall be within 10%					
Damp Heat Load/ Humidity Load	The specimen should be subjected to 40°C,90 to 95%RH environment, and the maximum allowable voltage applied for 1000 hours, then stored at room temperature and humidity for one or two hours. The change of varistor voltage shall be within 10%.					
Low Temperature Storage	The specimen should be subjected to -40°C, without load for 1000 hours and then stored at room temperature for one two hours. The change of varistor voltage shall be within 10%.					



SOLDERING RECOMMENDATIONS

	Reflow Condition	Lead-free assembly
	Temperature Max (T _{s(min)})	150°C
Pre Heat	Temperature Max (T _{s(max)})	200°C
	Time (min to max) (t _s)	60 – 180 secs
Average ran	np up rate (Liquidus Temp ($T_{\scriptscriptstyle L}$) to peak	3°C/second max
	T _{s(max)} to T _L - Ramp-up Rate	3°C/second max
Reflow	Temperature (T _L) (Liquidus)	217°C
Renow	Time (min to max) (t_L)	60 – 150 seconds
Peak Temp	erature (T _P)	260°C
Time within	n 5°C of actual peak Temperature (t_p)	20 – 40 seconds
Ramp-down Rate		6°C/second max
Time 25°C t	co peak Temperature (T _P)	8 minutes max.
Do not exce	eed	260°C

DIMENSION SPECIFICATION



Size	L(mm)	W(mm)	T(mm)	M(mm)
0603	1.6±0.15	0.8±0.15	≤ 0.9	0.35±0.10

ORDERING INFORMATION

Part Number	Package&Size	QTY/Reel	Reel Size
SME0603B12LA	0603 (1.6 x 0.8 mm)	4000PCS	7"



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By QR Code





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SME0603B12LA

Multilayer Chip Varistor For ESD Protection