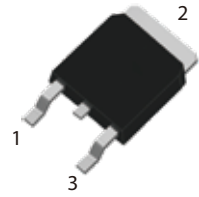


FEATURES

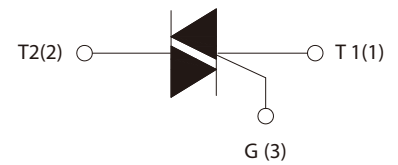
- | Direct interfacing to logic level ICs
- | Direct interfacing to low power gate drive circuits
- | High blocking voltage capability
- | Planar passivated for voltage ruggedness and reliability
- | Triggering in all four quadrant



TO-252

APPLICATIONS

- | General purpose motor control circuits
- | Phase control operations in light dimmers and motor speed controllers
- | Home appliances



Schematic Symbol

ABSOLUTE MAXIMUM RATINGS

| Parameter | Symbol | Value | Unit |
|---|---------------------|----------|------------------------|
| Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$) | V_{DRM} | 600 | V |
| Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$) | V_{RRM} | 600 | V |
| RMS on-state current ($T_c=100^\circ\text{C}$) | $I_{\text{T(RMS)}}$ | 4 | A |
| Non repetitive surge peak on-state current (full cycle, $F=50\text{Hz}$) | I_{TSM} | 35 | |
| I^2t value for fusing ($t_p=10\text{ms}$) | I^2t | 6.1 | A^2S |
| Critical rate of rise of on-state current ($I_G=2*I_{GT}$) | I - II - III | 50 | $\text{A}/\mu\text{s}$ |
| | IV | 10 | |
| Peak gate current | I_{GM} | 2 | A |
| Average gate power dissipation | $P_{\text{G(AV)}}$ | 0.5 | W |
| Peak gate power | P_{GM} | 5 | W |
| Operating junction temperature range | T_j | -40~+125 | $^\circ\text{C}$ |
| Storage junction temperature range | T_{STG} | -40~+150 | |

ELECTRICAL CHARACTERISTICS ($T_j=25^\circ\text{C}$ unless otherwise specified)

| Symbol | Test Condition | Quadrant | Value | | | | Unit |
|-----------|--|-------------------------|------------|-----------|------------|------------|------------------|
| | | | T | D | E | F | |
| I_{GT} | $V_D=12V$ | I - II - III | ≤ 5 | ≤ 5 | ≤ 10 | ≤ 25 | mA |
| | | IV | ≤ 5 | ≤ 10 | ≤ 25 | ≤ 70 | |
| V_{GT} | | ALL | ≤ 1.3 | | | | V |
| V_{GD} | $V_D=V_{DRM}, R_L=3.3K\Omega, T_j=125^\circ\text{C}$ | | ≥ 0.2 | | | | V |
| I_H | $I_T=100\text{mA}$ | | ≤ 5 | ≤ 15 | ≤ 25 | ≤ 30 | mA |
| I_L | $I_G=1.2I_{GT}$ | I - III | ≤ 10 | ≤ 20 | ≤ 30 | ≤ 40 | |
| | | II - IV | ≤ 15 | ≤ 35 | ≤ 45 | ≤ 60 | |
| dV_D/dt | $V_D=67\%V_{DRM}, T_j=125^\circ\text{C}$ | | ≥ 20 | ≥ 50 | ≥ 100 | ≥ 150 | V/ μs |
| V_{TM} | $I_{TM}=5.5A, t_p=380\mu\text{s}$ | | ≤ 1.6 | | | | V |
| I_{DRM} | $V_D=V_{DRM}, V_R=V_{RRM}$ | $T_j=25^\circ\text{C}$ | ≤ 5 | | | | μA |
| I_{RRM} | | $T_j=125^\circ\text{C}$ | ≤ 0.5 | | | | mA |

THERMAL RESISTANCES

| Symbol | Parameter | Value | Unit |
|---------------|----------------------|-------|--------------------|
| $R_{th(j-c)}$ | Junction to case(AC) | 2.8 | $^\circ\text{C}/W$ |

PARAMETER CHARACTERISTIC CURVE

FIG.1 Maximum power dissipation versus RMS on-state current

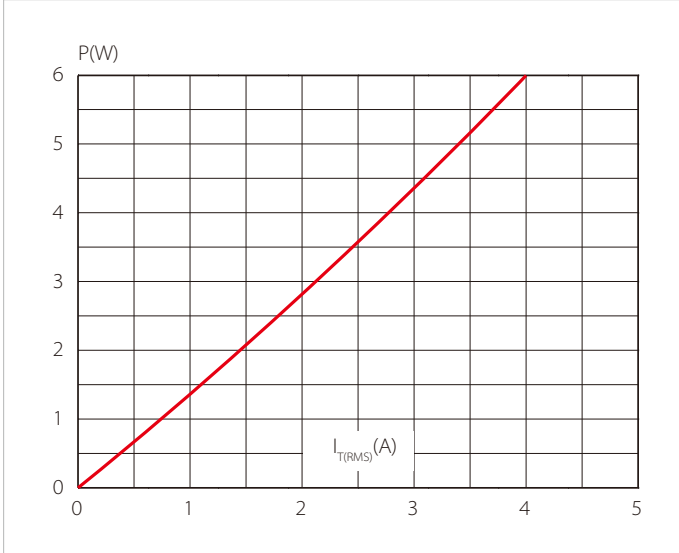


FIG.2: RMS on-state current versus ambient temperature (printed circuit board FR4, copper thickness: 35 μ m)

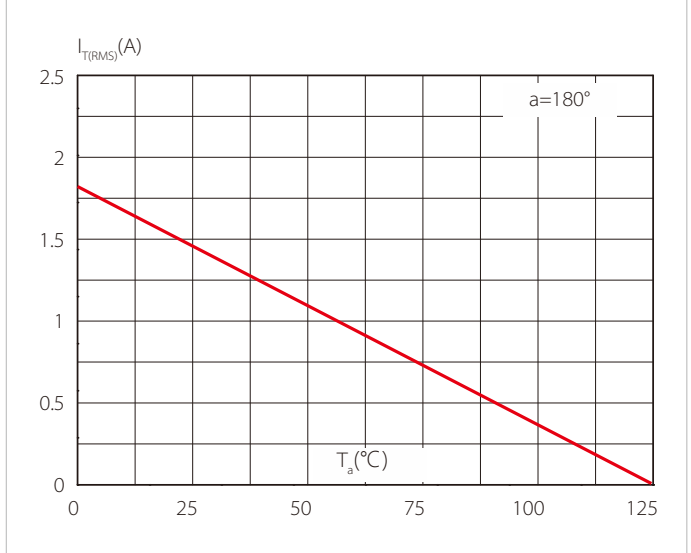


FIG.3: Surge peak on-state current versus number of cycles

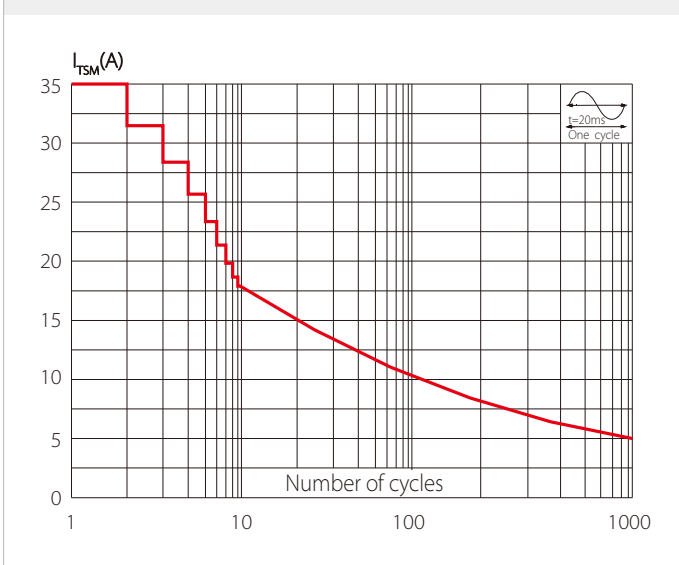


FIG.4 On-state characteristics (maximum values)

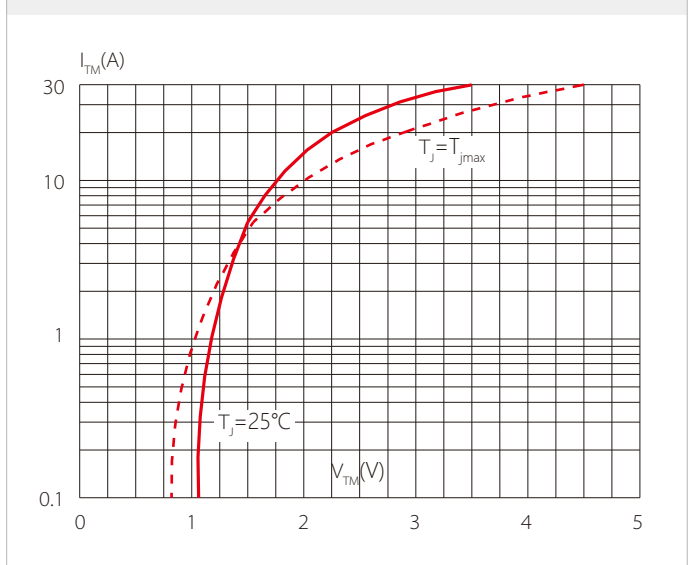


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$ and corresponding value of I^2t ($di/dt < 50\text{A}/\mu\text{s}$)

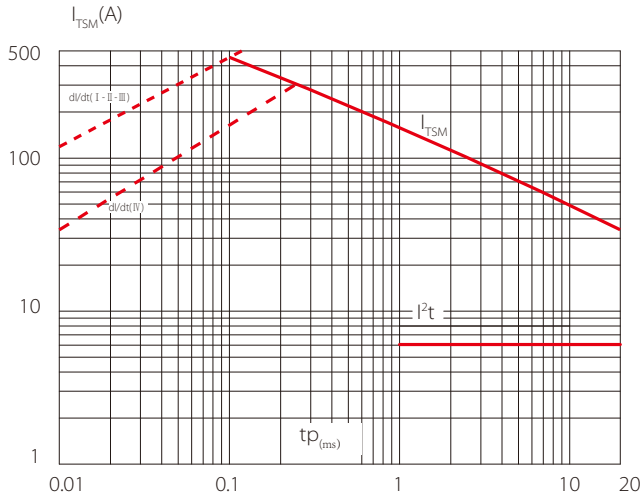


FIG.6 Relative variations of gate trigger current versus junction temperature

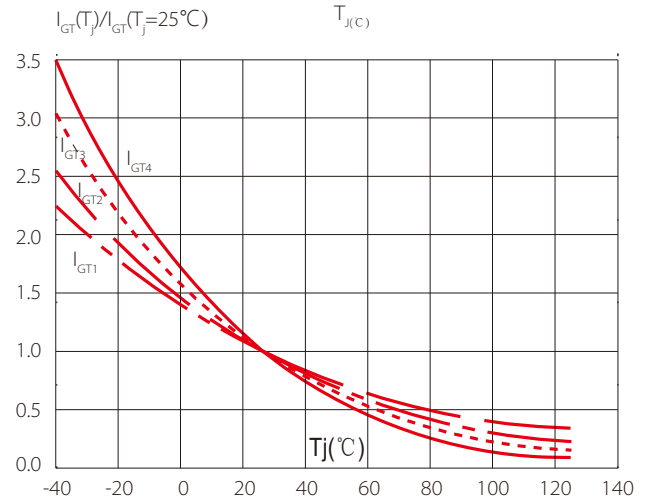


FIG.7 Relative variations of holding current versus junction temperature

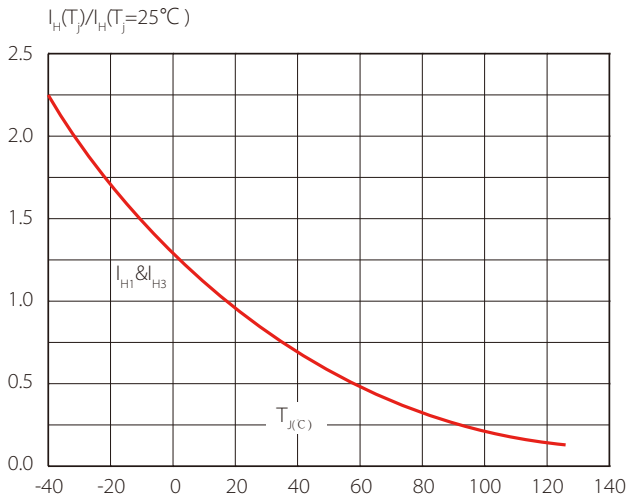
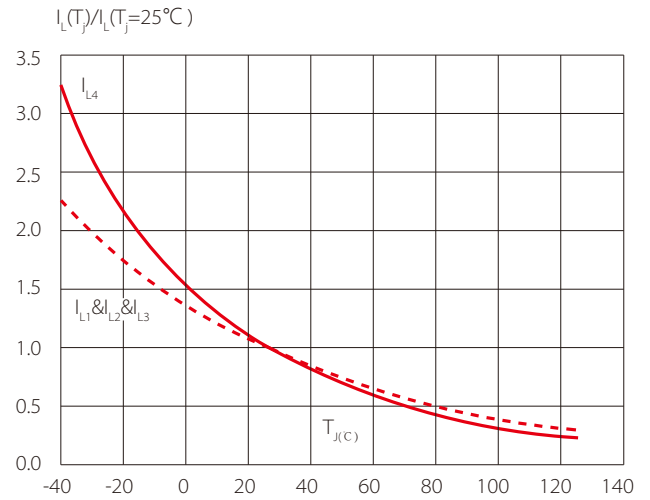
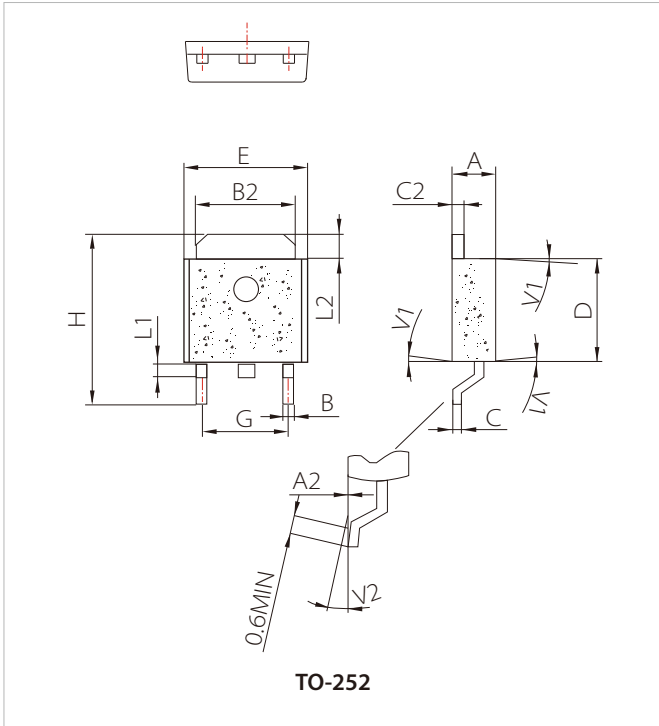


FIG.8 Relative variations of latching current versus junction temperature



PACKAGE MECHANICAL DATA



| Ref. | Dimensions | | | | | |
|------|-------------|------|------|--------|-------|-------|
| | Millimeters | | | Inches | | |
| | Min. | Typ. | Max. | Min. | Typ. | Max. |
| A | 2.20 | | 2.40 | 0.086 | | 0.095 |
| A2 | 0.03 | | 0.23 | 0.001 | | 0.009 |
| B | 0.55 | | 0.65 | 0.022 | | 0.026 |
| B2 | 5.10 | | 5.40 | 0.200 | | 0.213 |
| C | 0.45 | | 0.62 | 0.018 | | 0.024 |
| C2 | 0.48 | | 0.62 | 0.019 | | 0.024 |
| D | 6.00 | | 6.20 | 0.236 | | 0.244 |
| E | 6.40 | | 6.70 | 0.252 | | 0.264 |
| G | 4.40 | | 4.70 | 0.173 | 0.1 | 0.185 |
| H | 9.35 | | 10.6 | 0.368 | | 0.417 |
| L1 | 1.30 | | 1.70 | 0.051 | 0.143 | 0.067 |
| L2 | 1.37 | | 1.50 | 0.054 | | 0.059 |
| L1 | | 4° | | | 0.130 | |
| V2 | 0° | | 8° | 0° | | 8° |

ORDERING INFORMATION

| Part Number | Package | QTY/Reel | Reel Size |
|-----------------|---------|----------|-----------|
| STD4Q60T(D/E/F) | TO-252 | 2500CS | 13" |

Headquarters

No.3387 Shendu Road Pujiang
I&E Park
Minhang Shanghai China
201000

Hotline

400-021-5756

Web

<https://www.semiware.com>

Sales center

Tel: 86-21-3463-7458
Email: sales18@semiware.com

Customer Service

Tel: 86-21-5484-1001
Email: sales17@semiware.com

Technical Support

Tel: 86-21-3463-7654
Email: fae01@semiware.com

Complaint & Suggestions

Tel: 86-21-3463-7172
Ext: 8868
Email: cs03@semiware.com

By QR Code

Website



Wechat

To find your local partner within Semiware's global network: www.semiware.com

© 2022 Semiware Semiconductor Inc.

The content of this document has been carefully checked and understood. However, neither Semiware nor its subsidiaries assume any liability whatsoever for any errors or inaccuracies of this document and the consequences thereof. Published specifications are subject to change without notice. Product suitability for any area of application must ultimately be determined by the customer. In all cases, products must never be operated outside their published specifications. Semiware does not guarantee the availability of all published products. This disclaimer shall be governed by substantive Swiss law and resulting disputes shall be settled by the courts at the place of business of Semiware. Latest publications and a complete disclaimer can be downloaded from the Semiware website. All trademarks recognized.