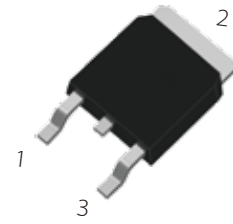


FEATURES

- | High current 8 A RMS current Triac
- | Low thermal resistance
- | High commutation or very high commutation capability
- | RoHS (2002/95/EC) compliant packages
- | UL-94, V0 flammability package resin compliance



TO-252



Schematic Symbol

APPLICATIONS

- | General purpose motor control circuits
- | Phase control operations in light dimmers and motor speed controllers
- | Home appliances

APPROVALS

| | |
|-------------|------------------------------------|
| RoHS | Compliance with 2011/65/EU |
| HF | Compliance with IEC61249-2-21:2003 |

THE MAIN PARAMETERS

| Symbol | Parameter | Value | Unit |
|--------------|-----------------------------------|-------|------|
| $I_{T(RMS)}$ | RMS on-state current | 8 | A |
| V_{DRM} | Off-state repetitive peak voltage | 800 | V |
| V_{TM} | On-state voltage | 1.5 | V |

ABSOLUTE MAXIMUM RATINGS

| Parameter | Symbol | Value | Unit |
|---|---------------------|----------|------------------|
| Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$) | V_{DRM} | 800 | V |
| Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$) | V_{RRM} | 800 | V |
| RMS on-state current ($T_c=100^\circ\text{C}$) | $I_{\text{T(RMS)}}$ | 8 | A |
| Non repetitive surge peak on-state current (full cycle, $F=50\text{Hz}$) | I_{TSM} | 80 | |
| I ² t value for fusing ($t_p=10\text{ms}$) | I^2t | 32 | A ² S |
| Critical rate of rise of on-state current ($I_G=2 \cdot I_{\text{GT}}$) | d/d_t | 50 | A/ μs |
| Peak gate current | I_{GM} | 4 | A |
| Average gate power dissipation | $P_{\text{G(AV)}}$ | 1 | W |
| Storage junction temperature range | T_{STG} | -40~+150 | °C |
| Operating junction temperature range | T_j | -40~+125 | |

ELECTRICAL CHARACTERISTICS ($T_j=25^\circ\text{C}$ unless otherwise specified)

| Symbol | Test Condition | Quadrant | Value | | | | Unit |
|------------------|--|-------------------------|------------|------------|------------|-------------|------------------|
| | | | TW | SW | CW | BW | |
| I_{GT} | $V_D=12\text{V}, R_L=33\Omega$ | I - II - III | ≤ 5 | ≤ 10 | ≤ 35 | ≤ 50 | mA |
| V_{GT} | | | ≤ 1.5 | | | | |
| V_{GD} | $V_D=V_{\text{DRM}}, R_L=3.3\text{K}\Omega, T_j=125^\circ\text{C}$ | | ≥ 0.2 | | | | V |
| I_{H} | $I_t=100\text{mA}$ | | ≤ 15 | ≤ 20 | ≤ 40 | ≤ 60 | mA |
| I_{L} | $I_G=1.2I_{\text{GT}}$ | I - III | ≤ 20 | ≤ 25 | ≤ 50 | ≤ 70 | |
| | | II | ≤ 25 | ≤ 35 | ≤ 70 | ≤ 90 | |
| dV_D/dt | $V_D=67\%V_{\text{DRM}}, T_j=125^\circ\text{C}$ | | ≥ 50 | ≥ 200 | ≥ 500 | ≥ 1000 | V/ μs |
| V_{TM} | $I_{\text{TM}}=11\text{A}, t_p=380\mu\text{s}$ | | ≤ 1.5 | | | | V |
| I_{DRM} | $V_D=V_{\text{DRM}}, V_R=V_{\text{RRM}}$ | $T_j=25^\circ\text{C}$ | ≤ 5 | | | | μA |
| I_{RRM} | | $T_j=125^\circ\text{C}$ | ≤ 1 | | | | mA |

THERMAL RESISTANCES

| Symbol | Parameter | Value | Unit |
|---------------|----------------------|-------|-----------------------------|
| $R_{th(j-c)}$ | Junction to case(AC) | 2.1 | $^{\circ}\text{C}/\text{W}$ |

PARAMETER CHARACTERISTIC CURVE

FIG.1 Maximum power dissipation versus RMS on-state current

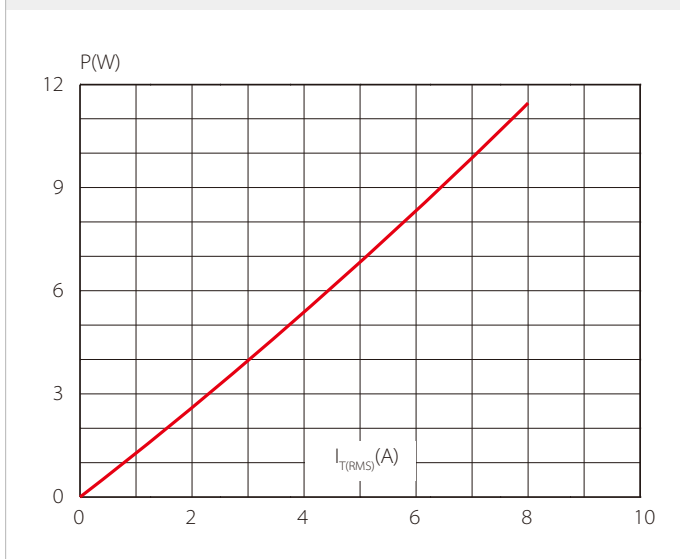


FIG.2: RMS on-state current versus ambient temperature (printed circuit board FR4, copper thickness:35 μm)(full cycle)

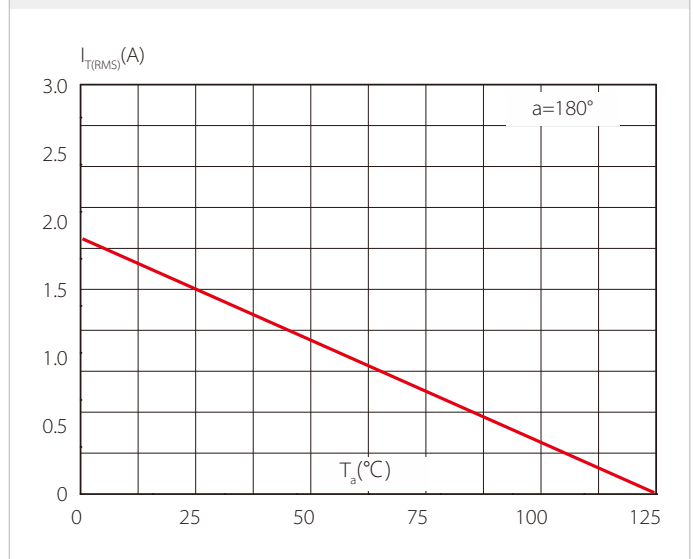


FIG.3: Surge peak on-state current versus number of cycles

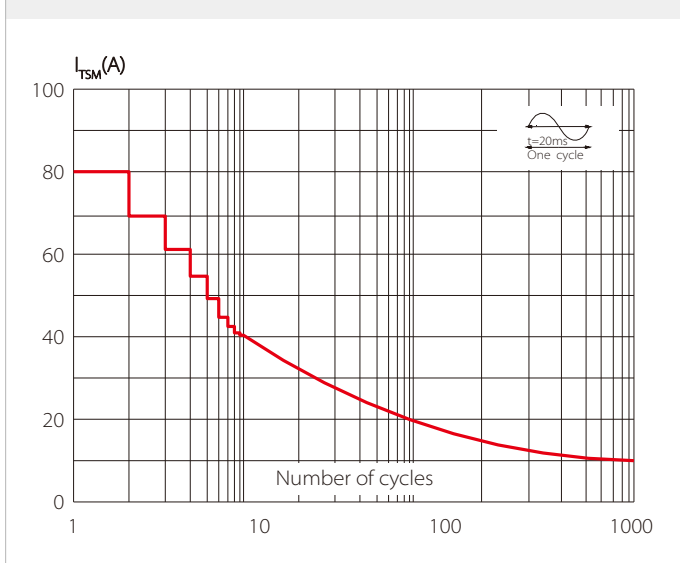


FIG.4 On-state characteristics (maximum values)

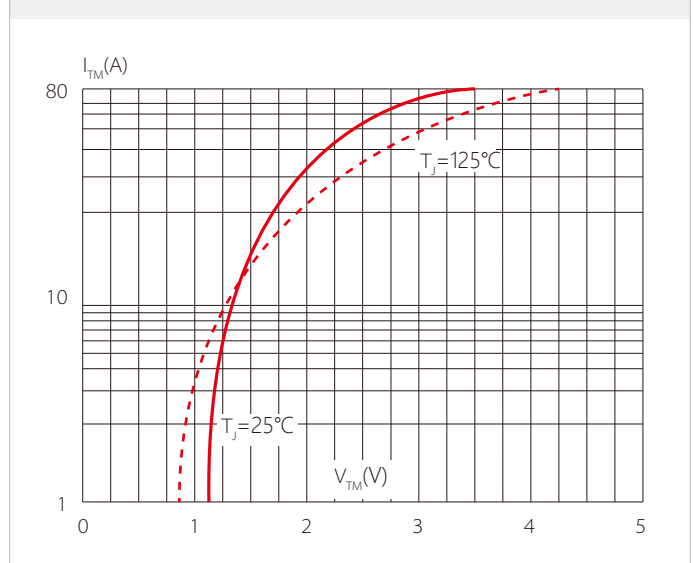


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$ and corresponding value of I^2t ($di/dt < 50\text{A}/\mu\text{s}$)

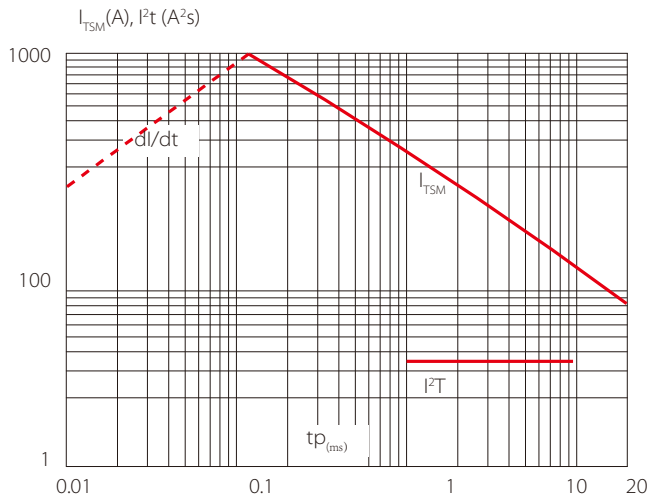


FIG.6 Relative variations of gate trigger current versus junction temperature

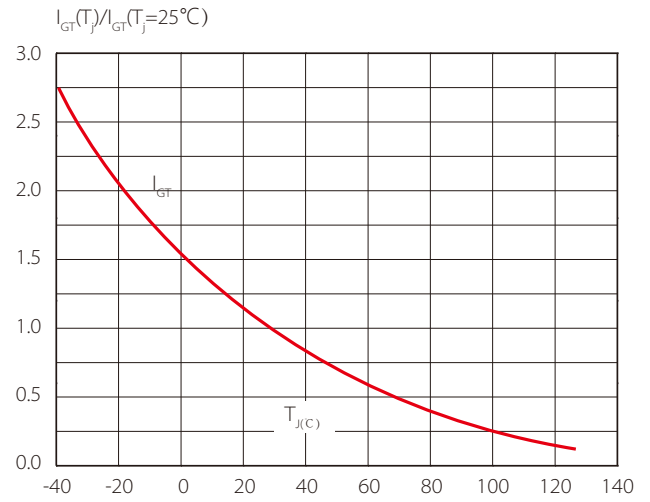


FIG.7 Relative variations of holding current versus junction temperature

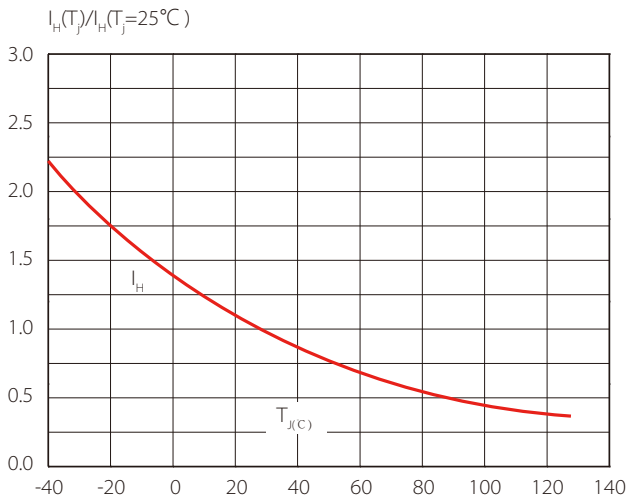
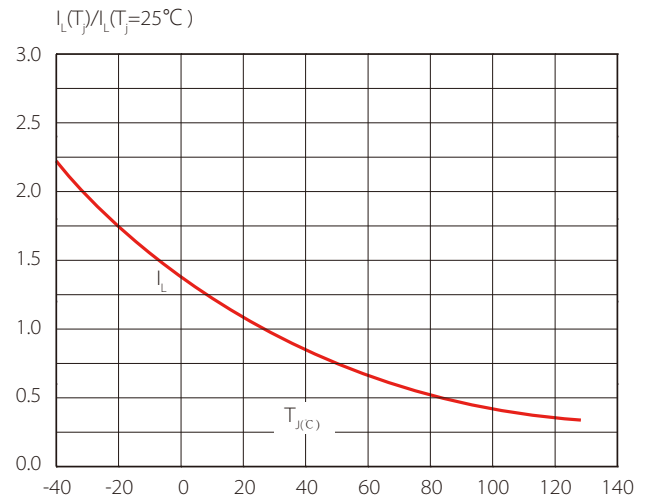
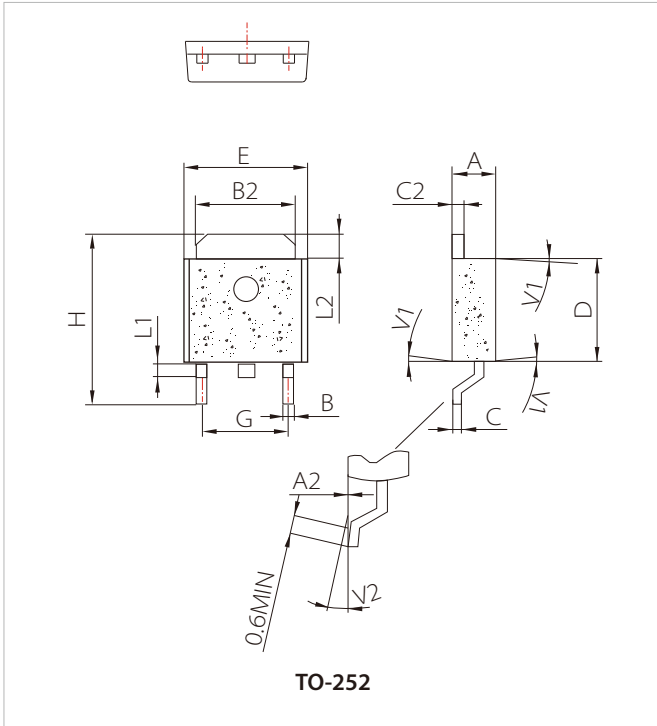


FIG.8 Relative variations of latching current versus junction temperature



PACKAGE MECHANICAL DATA



| Ref. | Dimensions | | | | | |
|------|-------------|------|------|--------|-------|-------|
| | Millimeters | | | Inches | | |
| | Min. | Typ. | Max. | Min. | Typ. | Max. |
| A | 2.20 | | 2.40 | 0.086 | | 0.095 |
| A2 | 0.03 | | 0.23 | 0.001 | | 0.009 |
| B | 0.55 | | 0.65 | 0.022 | | 0.026 |
| B2 | 5.10 | | 5.40 | 0.200 | | 0.213 |
| C | 0.45 | | 0.62 | 0.018 | | 0.024 |
| C2 | 0.48 | | 0.62 | 0.019 | | 0.024 |
| D | 6.00 | | 6.20 | 0.236 | | 0.244 |
| E | 6.40 | | 6.70 | 0.252 | | 0.264 |
| G | 4.40 | | 4.70 | 0.173 | 0.1 | 0.185 |
| H | 9.35 | | 10.6 | 0.368 | | 0.417 |
| L1 | 1.30 | | 1.70 | 0.051 | 0.143 | 0.067 |
| L2 | 1.37 | | 1.50 | 0.054 | | 0.059 |
| L1 | | 4° | | | 0.130 | |
| V2 | 0° | | 8° | 0° | | 8° |

ORDERING INFORMATION

| Part Number | Package | QTY/Reel | Reel Size |
|---------------------|---------|----------|-----------|
| STD8A80TW(SW/CW/BW) | TO-252 | 2500CS | 13" |

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